

AMENDMENTS TO THE SPECIFICATION

Please amend the paragraph beginning at page 3, line 11 as follows:

A1
The present invention provides a computer system including a CPU for operating with pipe-line processing, a ROM storing a program to be executed by the CPU, and at least one ROM correction unit including a ~~[[fist]]~~ first storage unit for storing a subject address of an original instruction group in the program having a bug therein, a second storage unit for storing a modified instruction group for replacing the original instruction group by the modified instruction group having a branch address, a comparator for comparing a current address of a current instruction read from the ROM against the subject address, a selector for selecting the current address or the branch address based on a result of the comparison by the comparator, and a flag generator for setting a ROM correction flag when the selector selects the branch address.